

**JP1**

INTERNAL/EXTERNAL BATTERY SELECT	
INTERNAL 2-3	EXTERNAL 1-2
<input checked="" type="checkbox"/>	

**JP7**

EXTERNAL 1PPS INPUT TERMININATION	
1K = OFF	50 OHM = ON
<input checked="" type="checkbox"/>	

**JP13**

VCTCXO POWER INPUT SELECT	
+5V JP13 = OFF JP14 = ON	+12V JP13 = ON JP14 = OFF
<input checked="" type="checkbox"/>	

**JP26**

SERIAL PORT CONFIGURATION	
RS-232 JP26 = ON JP27 = ON	RS-422 JP26 = OFF JP27 = OFF
<input checked="" type="checkbox"/>	

**JP2**

WATCHDOG ENABLE/DISABLE	
ENABLE = ON	DISABLE = OFF
<input checked="" type="checkbox"/>	

**JP8**

-12V SOURCE SELECT	
INTERNAL = ON	EXTERNAL = OFF
<input checked="" type="checkbox"/>	

**JP15**

PLL INPUT CONFIGURATION	
1-5-10 OF 6,176 JP15 = OFF JP16 = ON	4,096 JP15 = ON JP16 = OFF
<input checked="" type="checkbox"/>	

**C8**

ANTENNA/DOWN CONVERTER CONFIGURATION	
ANTENNA C8 - INSTALLED R25 - NOT INSTALLED	DOWN CONV C8 - NOT INSTALLED R25 - INSTALLED
<input checked="" type="checkbox"/>	

**JP3**

AUXILIARY INTERNAL OSCILLATOR CONTROL	
DAC 2-1	PLL 2-3
<input checked="" type="checkbox"/>	

**JP9**

EXTERNAL 1-5-10 INPUT TERMININATION	
1K = OFF	50 OHM = ON
<input checked="" type="checkbox"/>	

**JP17**

ANTENNA FEED POWER SELECT	
+5V JP17 = OFF JP18 = ON	+12V JP17 = ON JP18 = OFF
<input checked="" type="checkbox"/>	

**JP4**

VCTCXO CONTROL	
DAC 2-1	PLL 2-3
<input checked="" type="checkbox"/>	

**JP10**

EXTERNAL AUX OSC INPUT TERMININATION	
1K = OFF	50 OHM = ON
<input checked="" type="checkbox"/>	

**JP19**

DC-DC CONVERTER CONFIGURATION	
*12 & *5 +/-12 & +5 JP19 = OFF JP20 = OFF JP21 = OFF JP22 = OFF	*12V +24V JP19 = ON JP20 = OFF JP21 = ON JP22 = OFF
<input checked="" type="checkbox"/>	

**JP5**

DAC OUTPUT VOLTAGE	
0-10 = ON	+/- 5 = OFF
<input checked="" type="checkbox"/>	

**JP11**

+12V SOURCE SELECT	
INTERNAL 2-3	EXTERNAL 2-1
<input checked="" type="checkbox"/>	

**JP23**

SVEESIX POWER INPUT SELECT	
5V JP23 = ON JP24 = OFF	9-32V JP23 = ON JP24 = ON
<input checked="" type="checkbox"/>	

**JP6**

AUXILIARY OSCILLATOR SOURCE	
INTERNAL 2-3	EXTERNAL 2-1
<input checked="" type="checkbox"/>	

**JP12**

+5V SOURCE SELECT	
INTERNAL = ON	EXTERNAL = OFF
<input checked="" type="checkbox"/>	

**JP25**

LCA DEBUG	
DEBUG = ON	NORMAL = OFF
<input checked="" type="checkbox"/>	

- ✓ = FUNCTION SELECTED
- INSTALL JUMPER ON ONE PIN WHEN FUNCTION "OFF" IS SELECTED.
- FOR JUMPER AND PART LOCATION REFER TO 86-600 SHEET 3 & 4.
- INSTALL THE FOLLOWING JUMPERS:  
P46-1 TO PAD C (IRIG-B to P9)  
P46-25 TO PAD E (1PPS to P10)

CHECKED BY:	DAA		
APPROVED BY:	<i>Raq</i>		
DATE:	12-4-97		
TITLE	SIZE	NUMBER	REV
87-6XX CONFIGURATION	A	87-610	F
DATE	12-04-97	DRAWN BY	KRK
FILENAME	610.CDR	SHEET	2 of 2